

DATA ACKNOWLEDGMENT USING IMPEDANCE MISMATCHING

Abstract of the Disclosure

A structure and associated method to control a flow of data on a semiconductor device.

A transmitter, receiver and transmission line are formed within the semiconductor device. The

5 transmitter, receiver, and transmission line are adapted to control data transfer between a first core and a second core within the semiconductor device. The transmitter is adapted to send a signal over the transmission line to the receiver adapted to receive the signal. The receiver is further adapted to create an impedance mismatch to indicate that the second core is unable to transfer the data. The transmitter is adapted to detect the impedance mismatch.